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**PATENT APPLICATION**

*JFW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Fumiaki KARASAWA et al.

Group Art Unit: 2822

Application No.: 10/642,737

Examiner: P. PERKINS

Filed: August 19, 2003

Docket No.: 116884

For: SEMICONDUCTOR APPARATUS AND THE MANUFACTURING METHOD, AND  
CIRCUIT BOARD AND ELECTRONIC DEVICE

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, Notice of Allowance or other action that closes prosecution (e.g., Quayle Action).
- ☒ a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
- ☒ 2. English language Abstracts of the non-English language references are attached hereto.

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3. A computer-generated English language translation of the following Japanese Patent Publications have been obtained from the website of the Japanese Patent Office (<http://www.jpo.go.jp>), and are attached, but have not been reviewed for accuracy. See References 1-3.

Respectfully submitted,

  
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JAO:YSC/mdw

Date: June 24, 2005

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